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Notice of Allowability	Application No.	Applicant(s)
	10/773,089	DELLOW, ANDREW
	Examiner	Art Unit
	Minh Dieu Nguyen	2137
The MAILING DATE of this communication apperall claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI	(OR REMAINS) CLOSED in this or other appropriate communicat GHTS. This application is subject	application. If not included ion will be mailed in due course. THIS
1. X This communication is responsive to 8/27/2007.		
2. X The allowed claim(s) is/are <u>1-29</u> .		
<ul> <li>3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some* c) None of the:</li> <li>1. Certified copies of the priority documents have been received.</li> <li>2. Certified copies of the priority documents have been received in Application No.</li> <li>3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> </ul>		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		
4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.		
5. CORRECTED DRAWINGS ( as "replacement sheets") must be submitted.		
(a) 🔲 including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached		
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t		
6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
Attachment(s)	E Mating of Informa	al Datant Application
<ol> <li>Notice of References Cited (PTO-892)</li> <li>Notice of Draftperson's Patent Drawing Review (PTO-948)</li> </ol>	<ol> <li>5. ☐ Notice of Information</li> <li>6. ☒ Interview Summation</li> </ol>	
2. Motice of Dranperson's Faterit Drawing Neview (F10-940)	Paper No./Mail	Date
Information Disclosure Statements (PTO/SB/08),     Paper No./Mail Date	7. 🛛 Examiner's Ame	ndment/Comment
4. Examiner's Comment Regarding Requirement for Deposit	8. 🗵 Examiner's Statement of Reasons for Allowance	
of Biological Material	9. 🗌 Other	
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## **EXAMINER'S AMENDMENT**

1. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with E. Russell Tarleton on 11/8/07.

2. The application has been amended as follows:

## Claim 1

(Currently Amended) A semiconductor integrated circuit, comprising:

a plurality of selectable pathways inter-connected between a plurality of data sources and data destinations;

a cryptographic circuit connected to the selectable pathways and arranged to selectively receive data at an input from at least one of the data sources, to decrypt or encrypt the data in accordance with a key, and selectively provide the encrypted or decrypted data to at least one of the data destinations via an output;

an instruction circuit arranged to receive on a first input an instruction signal and to generate therefrom an output to control the plurality of selectable pathways to select a data pathway configuration of the circuit, thereby selecting from which of the data sources the cryptographic circuit receives data and to which destination the cryptographic circuit provides the data;

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wherein the instruction circuit is configurable by a limiting rule signal, received at a second input, to operate in accordance with the rule signal that limits the data pathway configurations that are selectable by the instruction signal, thereby limiting the flow of data between the data sources and data destinations.

## Claims 38-40 are canceled.

## Allowable Subject Matter

- 3. Claims 1-29 are allowed.
- 4. The following is an examiner's statement of reasons for allowance:

The present invention is directed to a semiconductor integrated circuit for securely selecting data pathways in an encryption or decryption system, in particular to securely routing encryption or decryption keys in a pay-television system. The independent claim 1 identifies the distinct feature of the instruction circuit is configurable by a limiting rule signal, received at a second input, to operate in accordance with the rule signal that limits the data pathway configurations that are selectable by the instruction signal, thereby limiting flow of data between the data sources and data destinations. By doing so, the present invention provides several advantages over prior designs (as submitted by the Applicant in the Remarks dated 8/27/2007, page 14). The closest prior arts fail to anticipate or render the above limitations obvious.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

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accompany the issue fee. Such submissions should be clearly labeled "Comments on

Statement of Reasons for Allowance."

6. Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Minh Dieu Nguyen whose telephone number is 571-272-

3873.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Emmanuel Moise can be reached on 571-272-3865. The fax phone number

for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for published

applications may be obtained from either Private PAIR or Public PAIR. Status

information for unpublished applications is available through Private PAIR only. For

more information about the PAIR system, see <a href="http://pair-direct.uspto.gov">http://pair-direct.uspto.gov</a>. Should you

have questions on access to the Private PAIR system, contact the Electronic Business

Center (EBC) at 866-217-9197 (toll-free).

guy almdun

MinhBieu Nguyen

Patent Examiner

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11/9/07